

1/11

FIG.1
(PRIOR ART)

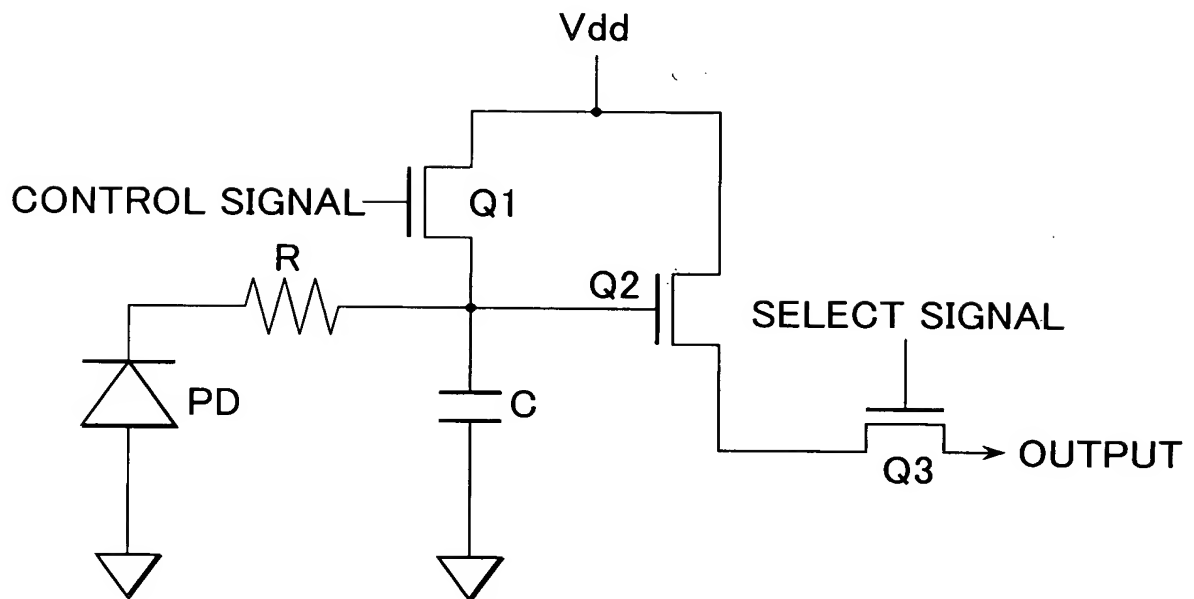


FIG.2A (PRIOR ART)

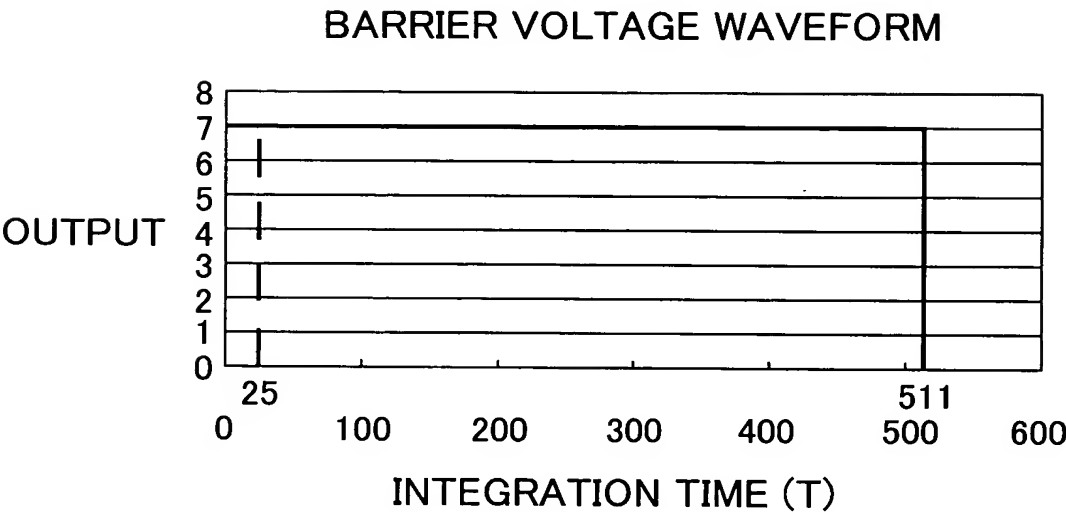
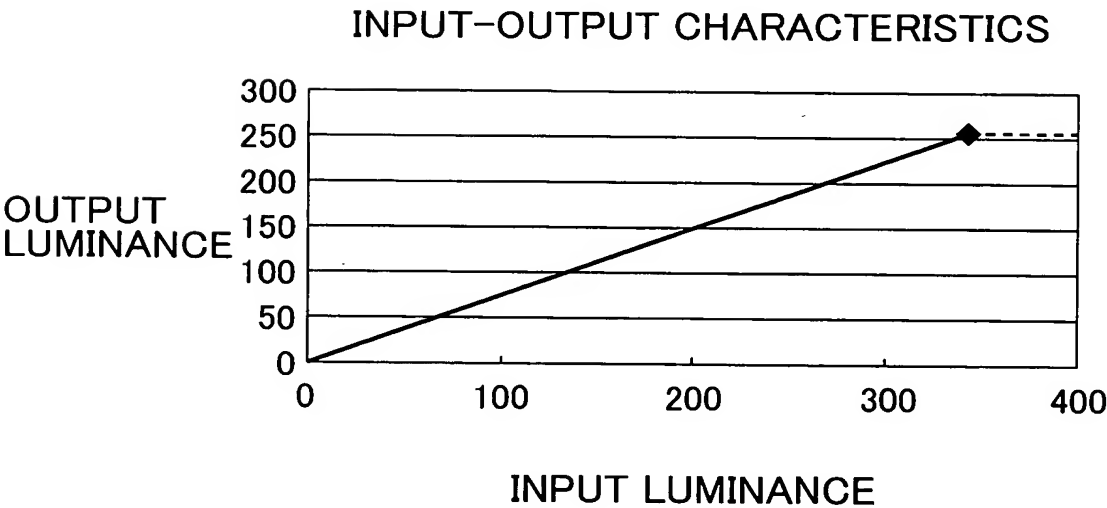


FIG.2B (PRIOR ART)



3/11

FIG.3A (PRIOR ART)

BARRIER VOLTAGE WAVEFORM

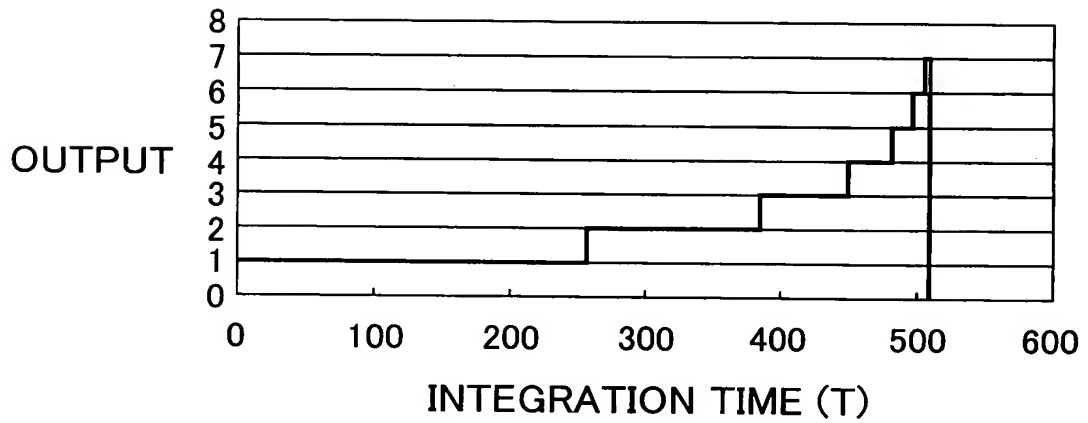


FIG.3B (PRIOR ART)

INPUT-OUTPUT CHARACTERISTICS

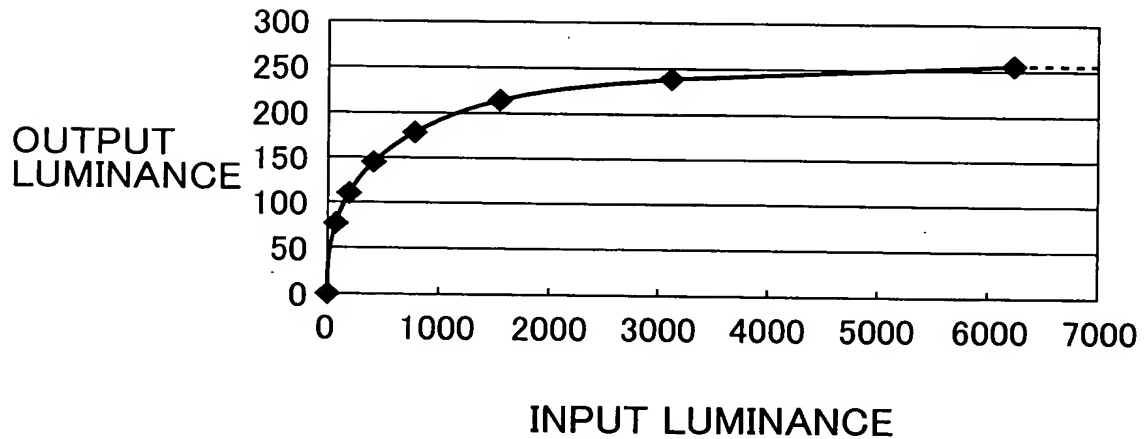


FIG.4
(PRIOR ART)

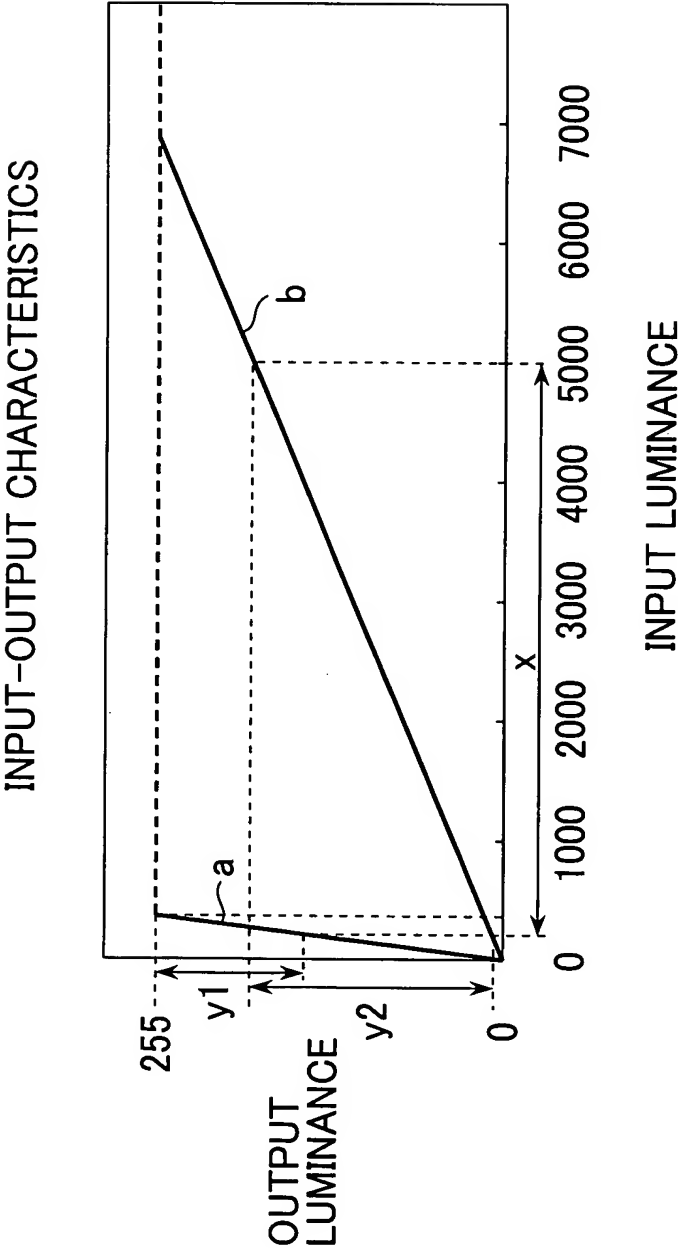
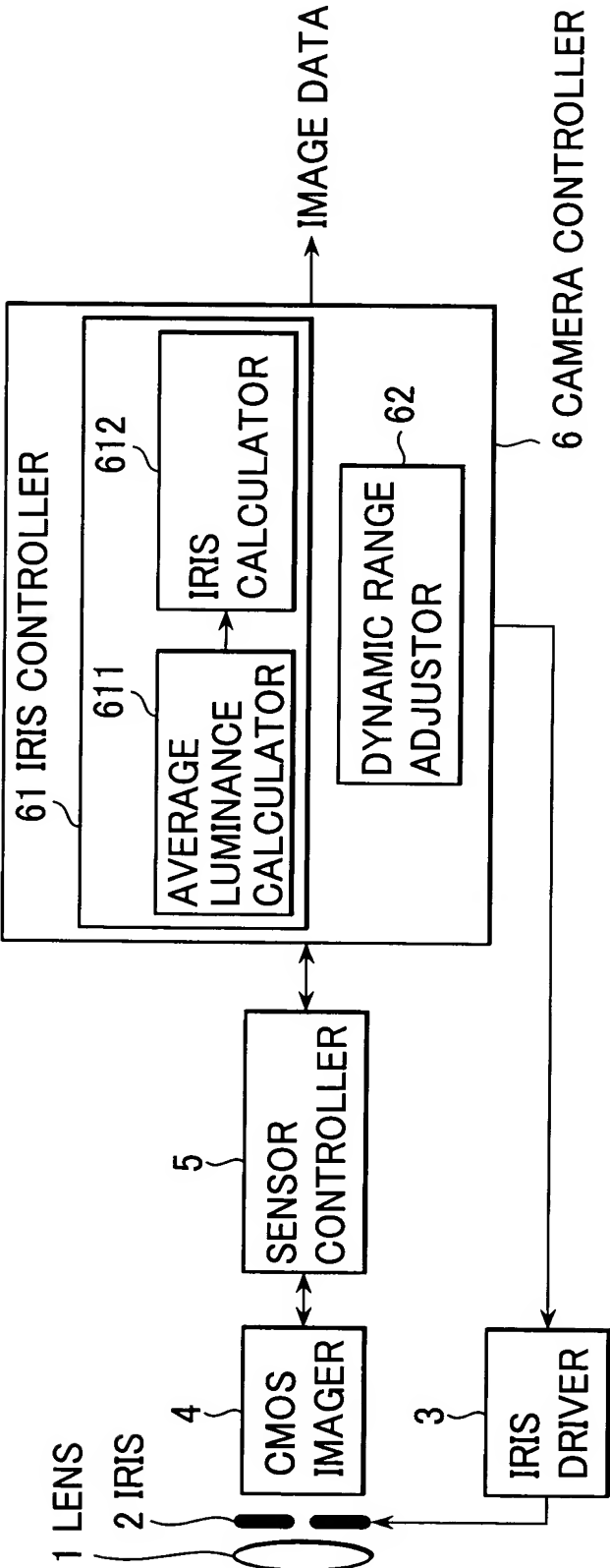
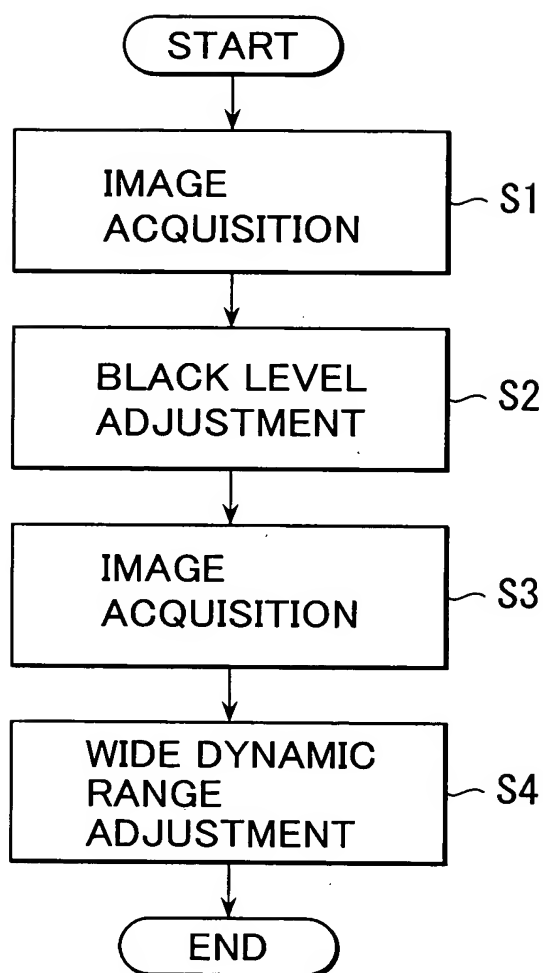


FIG.5



6/11

FIG.6



7/11

FIG.7

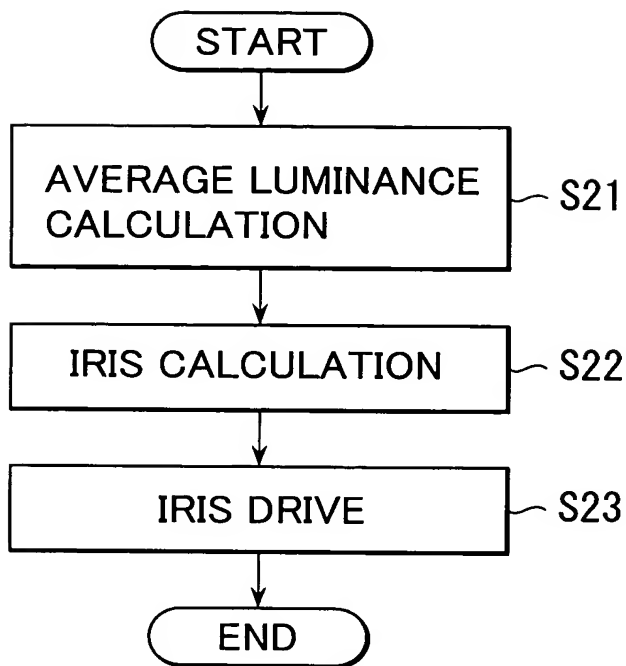
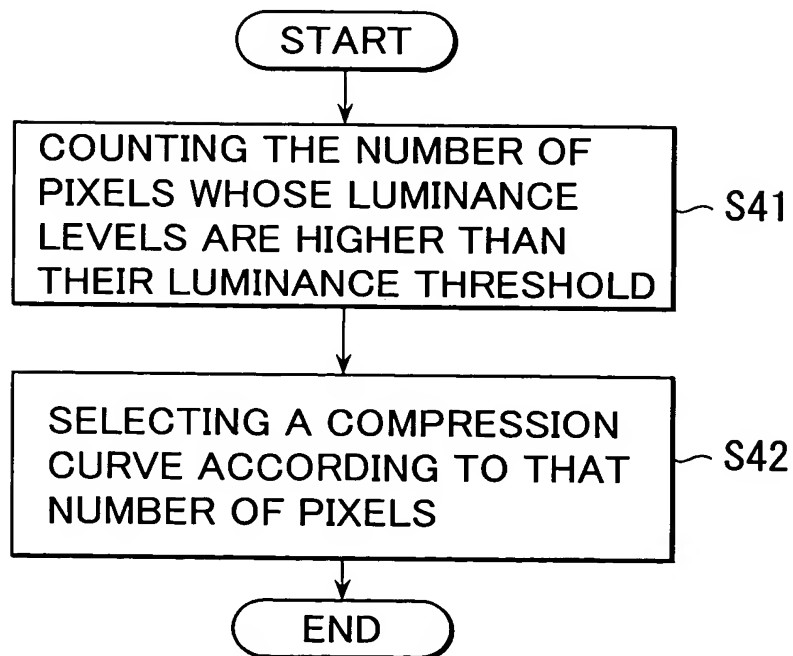


FIG.8



8/11

FIG.9

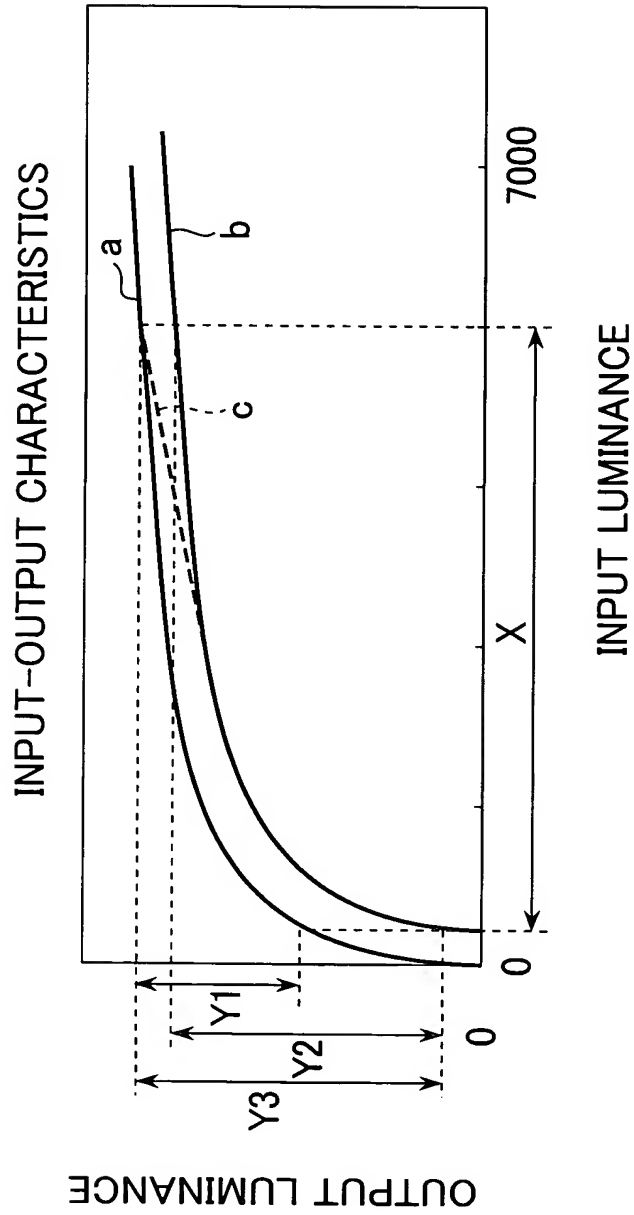
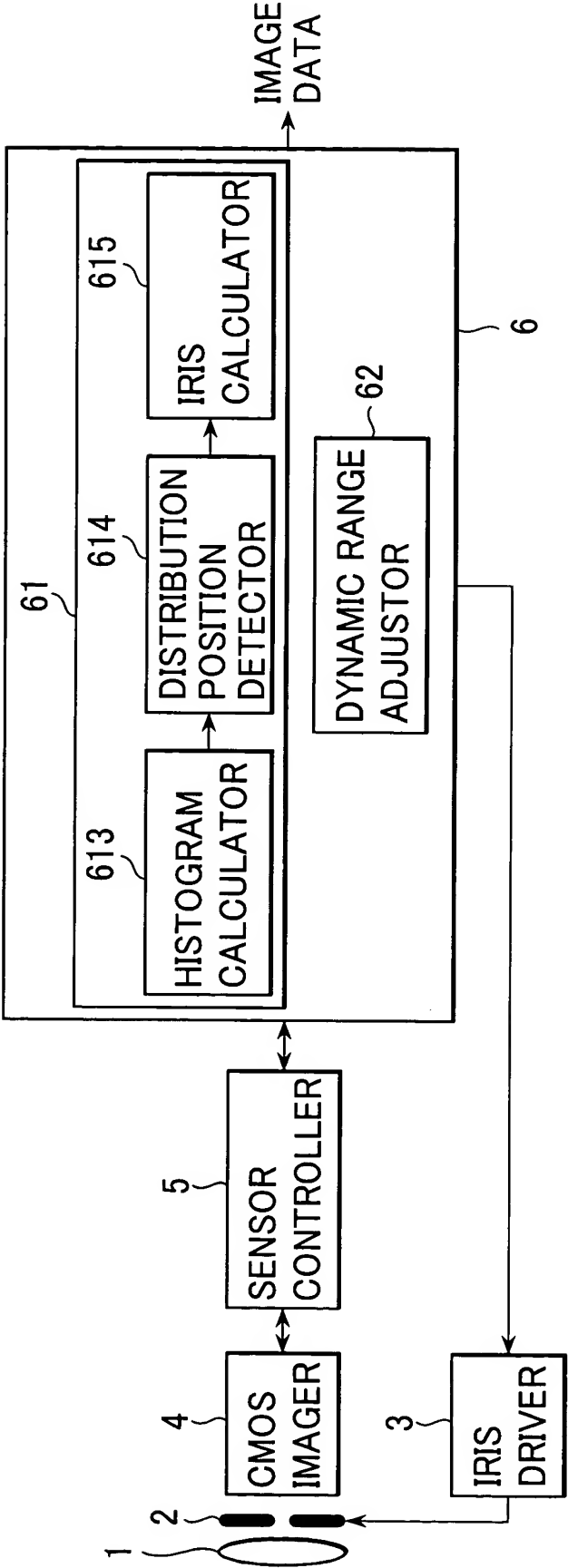
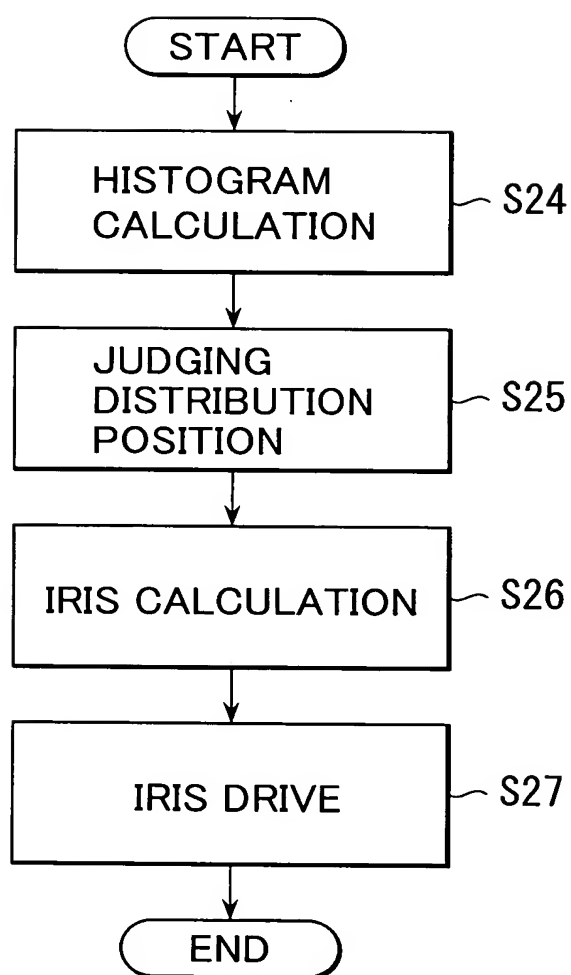


FIG.10



10/11

FIG.11



11/11

FIG.12A

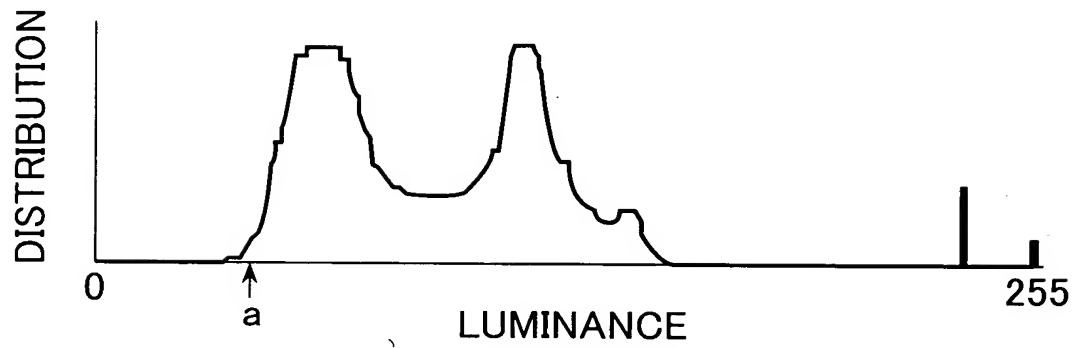


FIG.12B

